

PATENT APPLICATION

IE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Nobuaki HASHIMOTO

Application No.:

09/673,479

Docket No.:

107280

Filed: October 16, 2000

For:

BINDER, SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING

THE SAME, CIRCUIT BOARD, AND ELECTRONIC EQUIPMENT

REQUEST FOR CORRECTION OF PALM RECORDS

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

Registration No.

Joel S. Armstrong Registration No. 36,430 ECHNOLOGY CENTER 2800

JAO:JSA/kaw

Date: April 16, 2002

OLIFF & BERRIDGE, PLC P.O. Box 19928 Alexandria, Virginia 22320 Telephone: (703) 836-6400

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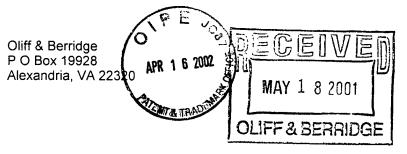
COMMISSIONER FOR PATENTS

UNITED STATES PATENT AND TRADEMARK OFFICE WASHINGTON, D.C. 2023

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 APPLICATION NUMBER
 FILING DATE
 GRP ART UNIT
 FIL FEE REC'D
 ATTY.DOCKET.NO
 DRAWINGS
 TOT CLAIMS
 IND CLAIMS

 09/673,479
 10/16/2000
 2812
 1084
 107280
 3
 28
 4



CONFIRMATION NO. 6925
CORRECTED FILING RECEIPT
OC000000000000077739

Date Mailed: 05/15/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Nobuaki Hashimoto, Nagano, JAPAN;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP00/00710 02/09/2000

Foreign Applications

JAPAN 11-039625(P) 02/18/1999

If Required, Foreign Filing License Granted 11/06/2000

Projected Publication Date: N/A

Non-Publication Request: No

Early Publication Request: No

of

Title

Binder, semiconductor device and method-er-manufacturing the same, circuit board, and electromic equipment *Electronic*

Preliminary Class

MAY -6 2002
TECHNOLOGY CENTER 2800